From the INTERNATIONAL BUREAU

PCT

NOTIFICATION OF THE RECORDING OF A CHANGE

(PCT Rule 92bis.1 and Administrative Instructions, Section 422)

To:

WATERMARK PATENT & TRADEMARK ATTORNEYS
4th Floor, "Durack Centre"
263 Adelaide Terrace
Perth, W.A. 6000
AUSTRALIE

Date of mailing (day/month/year)
04 October 2000 (04.10.00)

Applicant's or agent's file reference
P15633pcau

IMPORTANT NOTIFICATION

International application No.
PCT/AU00/00673

International filing date (day/month/year)
16 June 2000 (16.06.00)

1. The following indications appeared on record concerning:

[X] the applicant [X] the inventor [ ] the agent [ ] the common representative

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State of Nationality: AU
State of Residence: AU
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Facsimile No.:
Teleprinter No.:

2. The International Bureau hereby notifies the applicant that the following change has been recorded concerning:

[ ] the person [ ] the name [X] the address [ ] the nationality [ ] the residence

Name and Address
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45 Ventor Street
Scarborough, W.A. 6019
Australia

State of Nationality: AU
State of Residence: AU
Telephone No.:
Facsimile No.:
Teleprinter No.:

3. Further observations, if necessary:

4. A copy of this notification has been sent to:

[X] the receiving Office
[ ] the International Searching Authority
[X] the International Preliminary Examining Authority
[ ] the designated Offices concerned
[X] the elected Offices concerned
[ ] other:

The International Bureau of WIPO
34, chemin des Colombettes
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Authorized officer
F. Baechler
Telephone No.: (41-22) 338.83.38

003561373

From the INTERNATIONAL BUREAU

PCT

NOTIFICATION OF ELECTION

(PCT Rule 61.2)

To:

Commissioner
US Department of Commerce
United States Patent and Trademark Office, PCT
2011 South Clark Place Room CP2/5C24
Arlington, VA 22202
ETATS-UNIS D'AMERIQUE

in its capacity as elected Office

Date of mailing: 28 December 2000 (28.12.00)	
International application No.: PCT/AU00/00673	Applicant's or agent's file reference: P15633pcau
International filing date: 16 June 2000 (16.06.00)	Priority date: 17 June 1999 (17.06.99)
Applicant: HARMAN, Philip, Victor et al	

1. The designated Office is hereby notified of its election made:

[X] in the demand filed with the International preliminary Examining Authority on:

05 September 2000 (05.09.00)

[ ] in a notice effecting later election filed with the International Bureau on:

2. The election [X] was

[ ] was not

made before the expiration of 19 months from the priority date or, where Rule 32 applies, within the time limit under Rule 32.2(b).

The International Bureau of WIPO
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PATENT COOPERATION TREATY

PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference p15633	FOR FURTHER ACTION	See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416).
International application No. PCT/AU 00/00673	International filing date *(day/month/year)* 16 June 2000	Priority Date *(day/month/year)* 17 June 1999

International Patent Classification (IPC) or national classification and IPC

Int. Cl.[7] H04N 3/00, 7/01, 9/00

Applicant

1. DYNAMIC DIGITAL DEPTH RESEARCH PTY LTD et al

1. This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36.

2. This REPORT consists of a total of **3** sheets, including this cover sheet.

   ☐ This report is also accompanied by ANNEXES, i.e., sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT).

   These annexes consist of a total of sheet(s).

3. This report contains indications relating to the following items:

I	☒	Basis of the report
II	☐	Priority
III	☐	Non-establishment of opinion with regard to novelty, inventive step and industrial applicability
IV	☐	Lack of unity of invention
V	☒	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement
VI	☐	Certain documents cited
VII	☐	Certain defects in the international application
VIII	☐	Certain observations on the international application

Date of submission of the demand	Date of completion of the report
05 September 2000	22 September 2000
Name and mailing address of the IPEA/AU	Authorized Officer
AUSTRALIAN PATENT OFFICE PO BOX 200 WODEN ACT 2606 AUSTRALIA E-mail address: pct@ipaustralia.gov.au Facsimile No. (02) 6285 3929	MANISH RAJ Telephone No. (02) 6283 2175

INTERNATIONAL P. LIMINARY EXAMINATION REPORT

International application No.
PCT/AU 00/00673

I. Basis of the report

1. With regard to the **elements** of the international application:*

- [X] the international application as originally filed.
- [ ] the description, pages , as originally filed,
  pages , filed with the demand,
  pages , received on with the letter of .
- [ ] the claims, pages , as originally filed,
  pages , as amended (together with any statement) under Article 19,
  pages , filed with the demand,
  pages , received on with the letter of .
- [ ] the drawings, pages , as originally filed,
  pages , filed with the demand,
  pages , received on with the letter of .
- [ ] the sequence listing part of the description:
  pages , as originally filed
  pages , filed with the demand
  pages , received on with the letter of .

2. With regard to the **language**, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item.
These elements were available or furnished to this Authority in the following language which is:

- [ ] the language of a translation furnished for the purposes of international search (under Rule 23.1(b)).
- [ ] the language of publication of the international application (under Rule 48.3(b)).
- [ ] the language of the translation furnished for the purposes of international preliminary examination (under Rules 55.2 and/or 55.3).

3. With regard to any **nucleotide and/or amino acid sequence** disclosed in the international application, was on the basis of the sequence listing:

- [ ] contained in the international application in written form.
- [ ] filed together with the international application in computer readable form.
- [ ] furnished subsequently to this Authority in written form.
- [ ] furnished subsequently to this Authority in computer readable form.
- [ ] The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.
- [ ] The statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished

4. [ ] The amendments have resulted in the cancellation of:
   - [ ] the description, pages
   - [ ] the claims, Nos.
   - [ ] the drawings, sheets/fig

5. [ ] This report has been established as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**

* *Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rules 70.16 and 70.17).*

** *Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report*

V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. Statement

Novelty (N)	Claims	1-30	YES
	Claims		NO
Inventive step (IS)	Claims	1-30	YES
	Claims		NO
Industrial applicability (IA)	Claims	1-30	YES
	Claims		NO

2. Citations and explanations (Rule 70.7)

1. Claims 1-30 are novel and involve inventive step because none of the citations or obvious combination of citations teach the combination of "determining the RMS value of pixel, on adjacent lines and utilizing the RMS value to create the interpreted line, in a line doubling system."

2. Claims 1-30 have industrial applicability because the invention can be made or used in the industry.

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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International Bureau

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PCT

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PQ 1018 17 June 1999 (17.06.1999) AU

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(74) Agent: WATERMARK PATENT & TRADEMARK ATTORNEYS; 4th Floor, "Durack Centre", 263 Adelaide Terrace, Perth, W.A. 6000 (AU).

(81) Designated States *(national)*: AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW.

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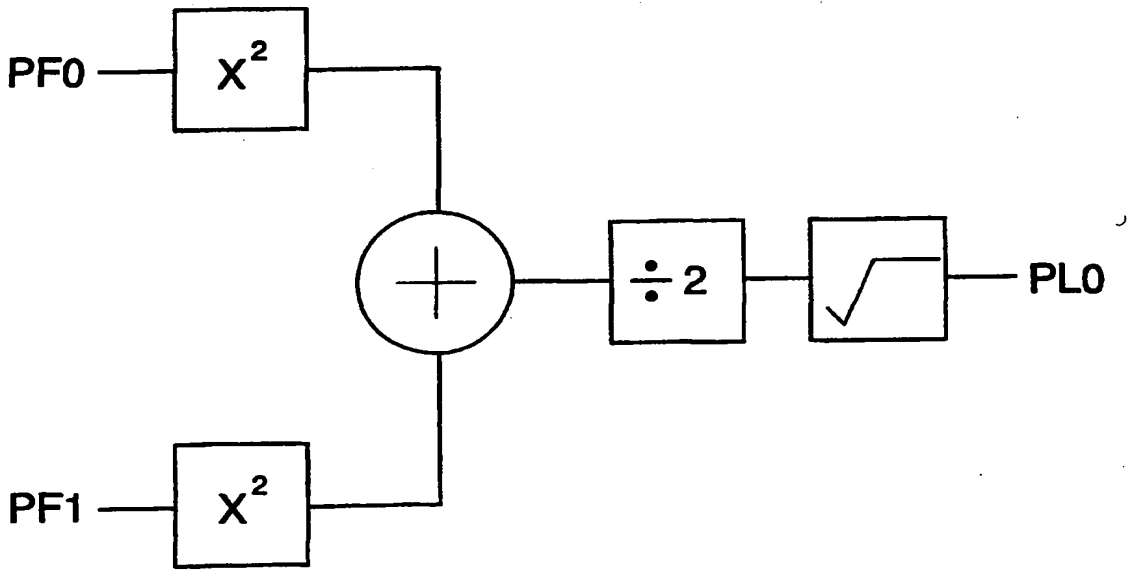
Published:
— *With international search report.*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: IMAGE ENHANCEMENT SYSTEM



(57) Abstract: A method of determining an interpreted line in a line doubling system including the steps of determining the value of pixels on adjacent lines, determining the Root Mean Square (RMS) value of the pixels on adjacent lines, and utilizing the RMS value to create the interpreted line.

WO 00/79781 A1

IMAGE ENHANCEMENT SYSTEM

FIELD OF THE INVENTION

The present invention relates to scan line doublers for increasing the number of apparent scan lines of a display device to reduce the visibility of the scan line structure of the picture image. More particularly, the present invention relates to a television, computer monitor or video projector scan line doubler which includes a method that overcomes the limitations of pixel interpolation by scan line averaging.

BACKGROUND OF THE INVENTION

When all other sources of error and distortion have been removed or minimised by correction or compensation, standard PAL or NTSC colour video images become limited in quality by perceptibility of the line scan structure.

Subjective visibility of the line scan structure is a direct consequence of the limited number of horizontal scan lines in the standard composite picture, and is further a direct consequence of the field by field interlace. A conventional PAL television frame at 25HZ repetition rate is composed of two fields eg F0 and F1. Each field includes 312.5 scan lines, each of which are separated by an unilluminated strip or band. Successive fields are offset so that the scan lines of the next field occupy the unilluminated strips of the present field. This arrangement is followed to minimize perception of 25Hz flicker in the resultant image.

The need to increase the number of scan lines is particularly evident in the application of Field Sequential 3D. In this instance, odd lines of the video image are used to carry the left eye image and even lines the right eye image. Thus, after de-multiplexing, the image intended for each eye is at half the resolution of the original video standard.

One approach to reducing the visibility of the line scan structure of the image calls for estimating, or interpolating, picture elements of additional scan lines from the picture elements already present in the picture image scanned in the conventional format. This prior approach is known in the art as "scan line doubling" or "line doubling", and calls for doubling the number of scan lines from 312.5 to 625 lines per field. Thus 625 lines are presented each 50th of a second.

One prior approach to pixel interpolation is carried out by an intra-field or spatial domain process. The pixel for the unilluminated band between two scan lines is derived as the average of the pixel amplitude and hue of the pixel in the scan line directly above and of the pixel in the scan line directly below. The main drawback of this approach is the reduced resolution or softness of the resultant picture in the vertical dimension at edges and some perceptible 25Hz vertical flicker in the instance of sharp vertical transitions within the picture image.

OBJECTIVES OF THE INVENTION

A general objective of the present invention is to provide an improved method and apparatus for television scan line doubling and display. The invention overcomes a number of limitations of the line averaging techniques of prior art and may be simply implemented in readily available hardware or software.

A more specific objective of the invention is to include a method whereby the additional pixel amplitude and hue may be determined via mathematical calculation or a lookup table and applied based upon specific characteristics of the overall image.

With the above objectives in mind, the present invention provides a method of determining an interpreted line in a line doubling system including the steps of:

determining the amplitude and hue of pixels on adjacent lines;

determining the Root Mean Square (RMS) value of the amplitude and hue of the pixels on adjacent lines;

utilizing the RMS value to create said interpreted line.

The Root mean Square value may be calculated in hardware or software for each set of adjacent pixels. Alternatively, a lookup table could be used to approximate the Root Mean Square value.

These and other objects, aspects, advantages and features of the present invention will be more fully understood and appreciated upon consideration of the following detailed description of a preferred embodiment, presented in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 depicts a segment of a video image.

Figure 2 depicts two tables showing average of two pixels, A and B, and the RMS of A and B.

Figure 3 depicts a hardware implementation of the invention.

Figure 4 depicts a simplified hardware implementation of the invention.

Figure 5 depicts the difference between two values A and B.

Figure 6 depicts a lookup table based upon the difference between A and B.

Figure 7 depicts a Pseudo RMS value of A and B.

Figure 8 depicts the true RMS value of A and B

DETAILED DESCRIPTION OF A PREFERRED EMBODIMENTS

Figure 1 depicts a segment of a video image consisting of Field Zero line zero (F0), Field One line one (F1) and interpreted lines L0 and L1.

The individual pixels per line are indicated such that the first pixel on line one of Field 0 is marked PF0,1 the second pixel PF0,2 and the nth pixel PF0,n. Similar terminology is used throughout the figure.

The prior art has described one method of adding additional lines to an interlaced image by inserting an additional pixel having a value equal to the average of the value of the pixel in the line above and the pixel in the line below i.e. if the pixel above has a value A and the pixel below has a value B then the additional pixel will have a value equal to:

New pixel = (A+B)/2

The main shortcoming of this technique is that the resultant image tends to look softer or slightly out of focus. This is direct limitation of using a simple averaging technique. For example, if we consider a black and white image as the input source and the above line to be peak white and the below line to be black then the interpolated line will be grey.

In order to overcome this limitation it is considered desirable to insert an interpolated line containing pixels that are more closely associated with the luminance value of the brighter of the two pixels A and B.

In a preferred embodiment this invention discloses a new technique to

achieve this by taking the Root Mean Square (RMS) of the values of A and B rather than the average.

This is illustrated in figure 2 which contains two tables. The first table depicts A along the X axis and B along the Y axis and the average of A and B at the intersection.

The second table depicts A along the X axis and B along the Y axis and the RMS value of A and B at the intersection. The RMS value is calculated from:

RMS Value $= \sqrt{\{A^2 + B^2)/2\}}$

By comparing the Average and RMS tables it is evident that:

1. When A = B, both mean and RMS process yield the same result;
2. When A > B or B > A then the result is squewed closer to the larger value, which is the desired result.

This RMS processing may be implemented in either hardware or software. A means of implementing the process in hardware is illustrated in figure 3. A pixel from a line in field F0 and a corresponding pixel from the line below in F1 are both passed simultaneously to squarer circuits. The output from each squarer is added and this result subsequently divided by two. This intermediate result has its square root taken and the resulting value becomes the new pixel.

If the image to be line doubled is in colour then the original image may well be in RGB format. If this is the case then each of the individual R, G and B values will require to be processed using the RMS method. Such a hardware implementation will require six squaring circuits and three square root circuits. Both the squaring and square root functions are comparatively difficult to implement in hardware. It is therefore another objective of this invention to disclose an alternative preferred embodiment that enables an RMS value to be calculated in an efficient and effective manner.

Figure 4 discloses an alternative preferred embodiment that simplifies the hardware implementation of the RMS process.

Figure 4 depicts a Read Only Memory (ROM) that requires an input address and provides data output dependant upon the input address. For illustrative purposes only, consider the pixel values to be quantised to 8 bits i.e. 256 individual levels.

The quantised pixels PF0,n and PF1,n are used to form an address for the ROM. At each unique address is stored a byte that approximates to the RMS value of PF0,n and PF1,n.

It is desirable that the RMS process can be implemented within an ASIC or FPGA. Using the ROM process of Figure 4, and assuming 8 bit RGB video then the number of input-output lines, external to the ASIC or FPGA, required to address the ROM's becomes excessive. Whilst a single ROM could be multiplexed across the R,G and B signals this may cause timing problems. In order to implement the RMS process within an ASIC or FPGA a simplified implementation is disclosed.

An alternative preferred embodiment that simplifies the look up requirement such that the RMS process could be implemented within an ASIC or FPGA may operate as follows:

Given above and below pixels A and B;

If A < B then swap A and B such that A is always greater than or equal to B;

If A = B then the new pixel = A;

Take the difference between A and B;

Use the difference to index into a lookup table;

Add the value from the lookup table to B;

Use this result as the value of the new pixel.

In a practical implementation, comprising 8 bit RGB video, the lookup table would be contained in a ROM and the difference information would be used as the address, which would typically be 8 bits, of the data located in the ROM. Thus in this implementation the ROM would contain a maximum of 256 addresses each containing an 8 bit value.

Figures 5 through 8 illustrate this simplified process as follows. In figure 5 the A value is horizontal and the B value vertical. The table contains the difference between A and B where A > B or A = B. Note: In order to simplify the explanation A and B are assumed to take values of between 0 and 100 in steps of 10.

In figure 6 the table depicts the value that would be stored in the lookup

table for each difference between A and B.

Figure 7 shows the effect of applying the previously disclosed method of approximating the RMS value of A and B. In this figure the A value is again horizontal and the B value vertical. The union of A and B within the table is the approximate RMS value of A and B, or a so called "Pseudo RMS" value.

If the results of figure 7 are compared with the true RMS values of A and B shown in figure 8 (which has been rounded up to zero decimal places) it will be seen that the percentage error in most cases is small and not significant in this particular application. This is due to the fact the eye is not sensitive to small variations in colour or intensity.

The objective of this invention is to overcome the previously described shortcomings of simply adding an additional pixel that is the mean of A and B.

This is a achieved by squewing the value of the additional pixel towards the larger value pixel.

Since, in the preferred embodiments, the value of the additional pixel is determined by deriving the RMS value of two existing pixels from vertically opposed lines and the RMS value determined, either derived accurately or an approximation, from a lookup table, then the values contained within the lookup table can be altered to provide the most aesthetically pleasing images.

In a preferred embodiment, different look up tables, or the same lookup table with different weightings, could be used depending upon the overall characteristics of the original image.

For example, should the overall image be particularly dark then it would be preferable to use interpolated pixels that are closer to the average value. Alternatively, if the image contains areas of high contrast then an alternative table, or the same table with different weightings, may be used with values that enhance the contrast differences.

It will be appreciated by those skilled in the art that these techniques may be applied to the whole image or selectively over the image such that different areas of the image may use different look up tables, or apply different weightings. The use of different tables or weightings could be determined by, but not limited to, brightness, contrast, colour, shading, hue, saturation, or marked

differences between these values over the image being processed.

Whilst the method and apparatus of the present invention has been summarised and explained by an illustrative application in television line doubling, it will be appreciated by those skilled in the art that many widely varying embodiments and applications are within the teaching and scope of the present invention, and that the examples presented herein are by way of illustration only and should not be construed as limiting the scope of this invention.

THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOWS:

1. A method of determining an interpreted line in a line doubling system including the steps of:

determining the value of pixels on adjacent lines;

determining the Root Mean Square (RMS) value of the pixels on adjacent lines;

utilizing the RMS value to create said interpreted line.

2. A method as claimed in claim 1 wherein the step of determining the value of pixels on adjacent lines includes determining the amplitude and hue of said pixels.

3. A method of determining an interpreted line in a line doubling system including the steps of:

determining the amplitude and hue of pixels on adjacent lines;

determining the Root Mean Square (RMS) value of the amplitude and hue of the pixels on adjacent lines;

utilizing the RMS value to create said interpreted line.

4. A method of determining an interpreted line in a line doubling system including the steps of:

determining the value of R, G and B components of pixels on adjacent lines;

determining the Root Mean Square (RMS) value of each said R, G and B components of the pixels on adjacent lines;

utilizing each RMS value to create said interpreted line

5. A method as claimed in any preceding claim wherein said RMS value of an interpreted pixel is determined by:

RMS Value $= \sqrt{\{A^2 + B^2)/2\}}$

Wherein A represents a pixel immediately above said interpreted pixel,

and B represents a pixel immediately below said interpreted pixel.

6. A method as claimed in any one of claims 1 to 4 wherein said RMS value is approximated.

7. A method as claimed in claim 6 wherein said approximate RMS value is determined from a lookup table.

8. A method of determining an interpreted line in a line doubling system including the steps of:

determining the value of pixels on adjacent lines;

accessing a memory device utilising the value of said adjacent pixels as an address for said memory device wherein an approximate Root Mean Square (RMS) value of the pixels on adjacent lines is stored at said address;

utilizing the RMS value to create said interpreted line.

9. A method as claimed in claim 8 wherein said memory device is a ROM.

10. A method of determining an interpreted line in a line doubling system including the steps of:

determining the value of pixels on adjacent lines;

consulting a lookup table to determine the approximate Root Mean Square (RMS) value of said adjacent pixels

utilising the RMS value to create said interpreted line.

11. A method of determining an interpreted line in a line doubling system including the steps of:

determining the value of a first pixel located above an interpreted pixel;

determining the value of a second pixel located below an interpreted pixel;

calculating the difference value between said first and second pixels;

utilising said difference value to index a lookup table and obtain a table value;

calculating an added value by adding said table value to the lesser of said first and second pixels;

assigning said calculated value to said interpreted pixel.

12. A method as claimed in claim 11 wherein said table value is predefined such that said added value is the approximate Root Mean Square (RMS) value of said first and second pixels.

13. A method as claimed in claim 11 or 12 wherein said table value is stored in a memory device and said difference value is the address to obtain said table value.

14. A method as claimed in any one of claims 11 to 13 wherein said difference value is calculated by:

comparing the value of said first and second pixels;

if said second pixel value is greater than said first pixel value then said first and second pixel values are interchanged;

subtracting said second pixel value from said first pixel value.

15. A method as claimed in any one of claims 11 to 13 wherein said difference value is calculated by:

comparing the value of said first and second pixels;

if said first pixel value is greater than said second pixel value then said first and second pixel values are interchanged;

subtracting said first pixel value from said second pixel value.

16. A method as claimed in any one of claims 7 to 13, including a plurality of lookup tables, and wherein the table to be utilised is selected based on the properties of the overall image.

17. A method as claimed in any one of claims 7 to 13 further including the step of adding or subtracting a constant to said added value, wherein said constant is

selected based on the properties of the overall image.

18. A system for determining an interpreted line in a line doubling system including:

an analysis means to determine the value of pixels on adjacent lines; and

a calculation means to determine the Root Mean Square (RMS) value of pixel values detected by said analysis means on said adjacent lines;

wherein said RMS value determined by said calculation means is utilised to create said interpreted line.

19. A system as claimed in claim 18 wherein said analysis means determines the amplitude and hue of the adjacent pixels.

20. A system as claimed in claim 18 or 19 wherein said analysis means determines the value of R, G and B components of the adjacent pixels, and said calculation means determines the RMS value for each said R, G and B component.

21. A system as claimed in any one of claims 18 to 20 wherein said calculation means determines the RMS value by:

RMS Value $= \sqrt{\{A^2 + B^2)/2\}}$

wherein A represents a pixel immediately above said interpreted pixel, and B represents a pixel immediately below said interpreted pixel.

22. A system as claimed in any one of claims 18 to 20 wherein said RMS value is approximated.

23. A system as claimed in claim 22 further including a memory means to store approximate RMS values and wherein said adjacent pixel values form an address to access said memory means.

24. A system as claimed in claim 23 wherein said memory means is a ROM.

25. A system as claimed in claim 22 further including a storage means to store approximate RMS values in a lookup table and wherein said adjacent pixel values form an address to access said lookup table.

26. A system for determining an interpreted line in a line doubling system including:

an analysis means to determine the value of a first pixel located above an interpreted pixel, and the value of a second pixel located below said interpreted;

a calculation means to determine a difference value between said first and second pixels;

a storage means to store a lookup table, wherein said difference value is an index to said lookup table, said index returning a table value;

an adding means to add said table value to the lesser of said first and second pixel values;

wherein the value returned from said adding means is assigned to said interpreted pixel.

27. A system as claimed in claim 26 wherein said lookup table is predefined such that the value returned from said adding means is the approximate RMS value of said first and second pixel values.

28. A system as claimed in any one of claims 25 to 27 wherein said storage means further includes a plurality of lookup tables, said system further including a selection means to select an appropriate table based on the properties of the overall image.

29. A system as claimed in any one of claims 25 to 27 wherein said adding means further adds or subtracts a constant to said table value, said constant being based on the properties of the overall image.

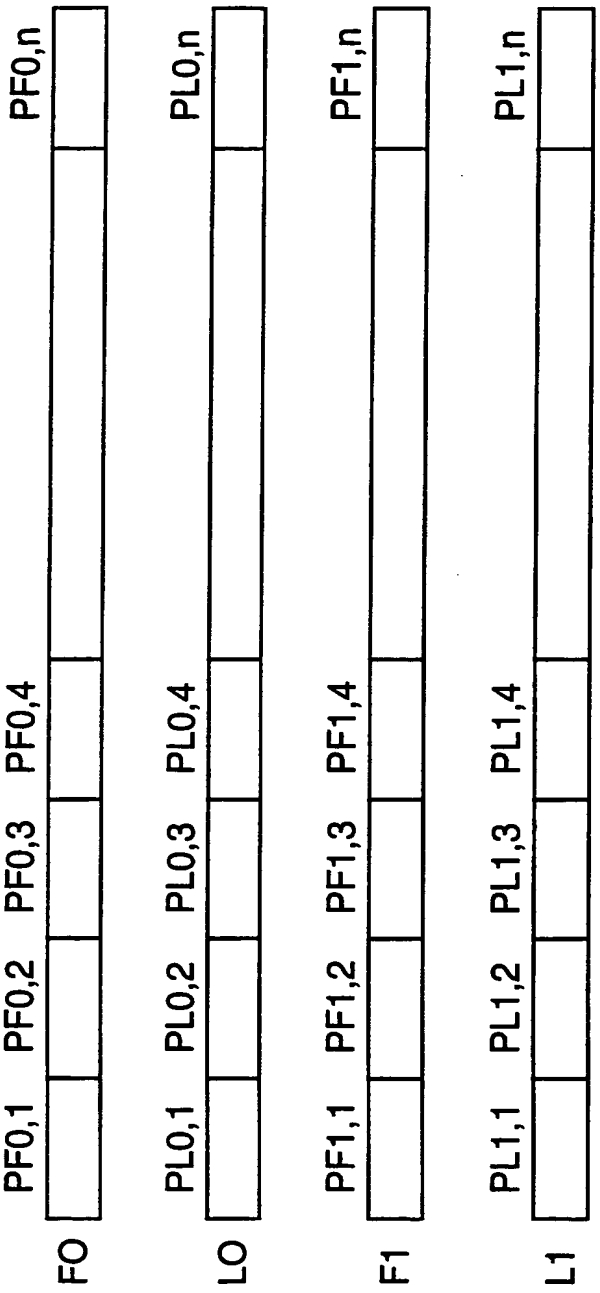
30. A method or system substantially as herein before described with reference to the accompanying drawings.

DATED this 15th day of June, 2000

DYNAMIC DIGITAL DEPTH RESEARCH PTY LTD

WATERMARK PATENT & TRADEMARK ATTORNEYS
4TH FLOOR, "DURACK CENTRE"
263 ADELAIDE TERRACE
PERTH W.A. 6000 AUSTRALIA

Fig 1.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PF0,2
PF0,3
PF0,4
PF0,n
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LO
PF1,1
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PF1,3
PF1,4
PF1,n
F1
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PL1,3
PL1,4
PL1,n
L1

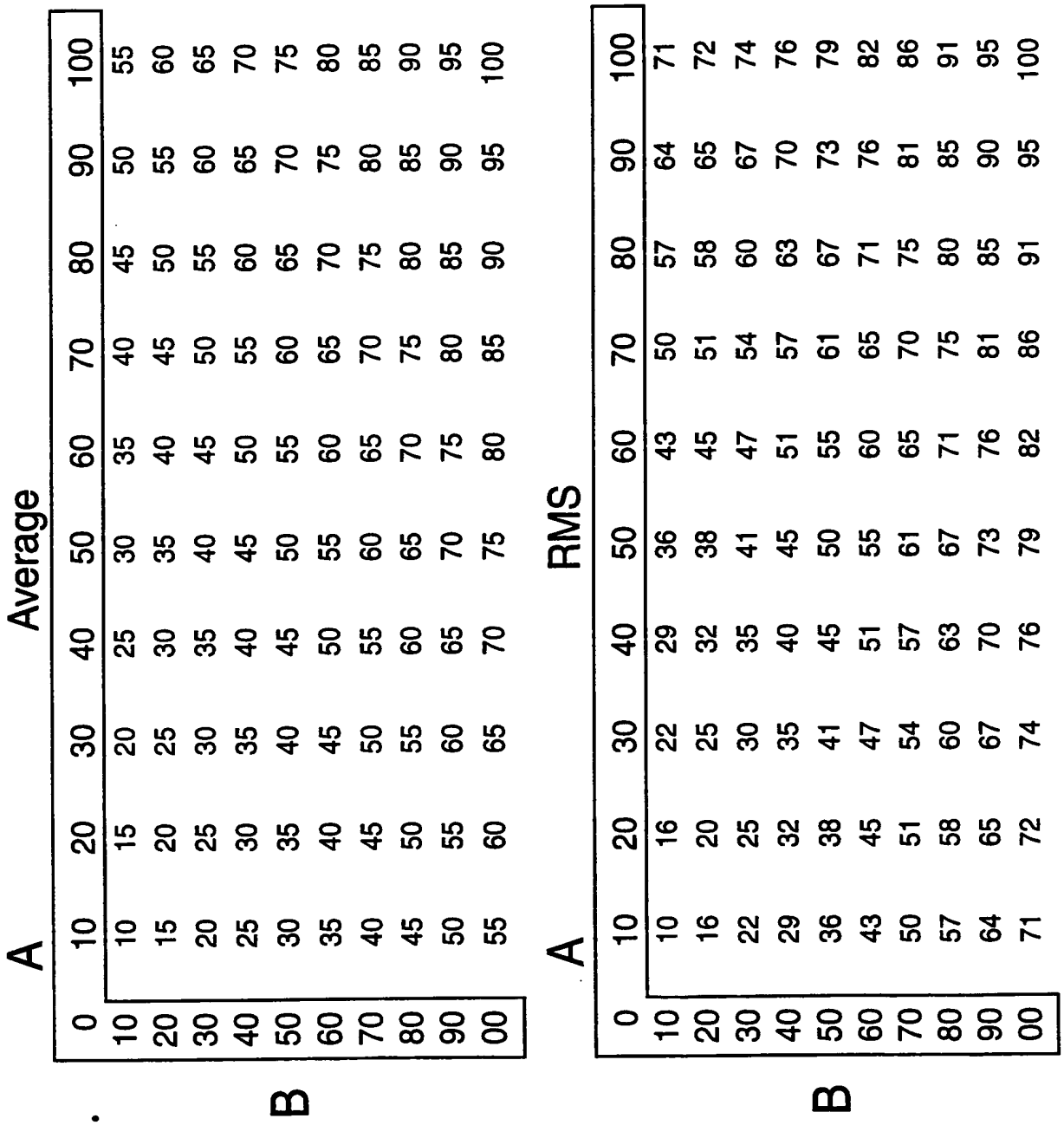

Average
A
0 10 20 30 40 50 60 70 80 90 100
10 10 15 20 25 30 35 40 45 50 55
20 15 20 25 30 35 40 45 50 55 60
30 20 25 30 35 40 45 50 55 60 65
40 25 30 35 40 45 50 55 60 65 70
50 30 35 40 45 50 55 60 65 70 75
60 35 40 45 50 55 60 65 70 75 80
70 40 45 50 55 60 65 70 75 80 85
80 45 50 55 60 65 70 75 80 85 90
90 50 55 60 65 70 75 80 85 90 95
00 55 60 65 70 75 80 85 90 95 100
B
RMS
A
0 10 20 30 40 50 60 70 80 90 100
10 10 16 22 29 36 43 50 57 64 71
20 16 20 25 32 38 45 51 58 65 72
30 22 25 30 35 41 47 54 60 67 74
40 29 32 35 40 45 51 57 63 70 76
50 36 38 41 45 50 55 61 67 73 79
60 43 45 47 51 55 60 65 71 76 82
70 50 51 54 57 61 65 70 75 81 86
80 57 58 60 63 67 71 75 80 85 91
90 64 65 67 70 73 76 81 85 90 95
00 71 72 74 76 79 82 86 91 95 100
B

Fig 2.

Fig 3.

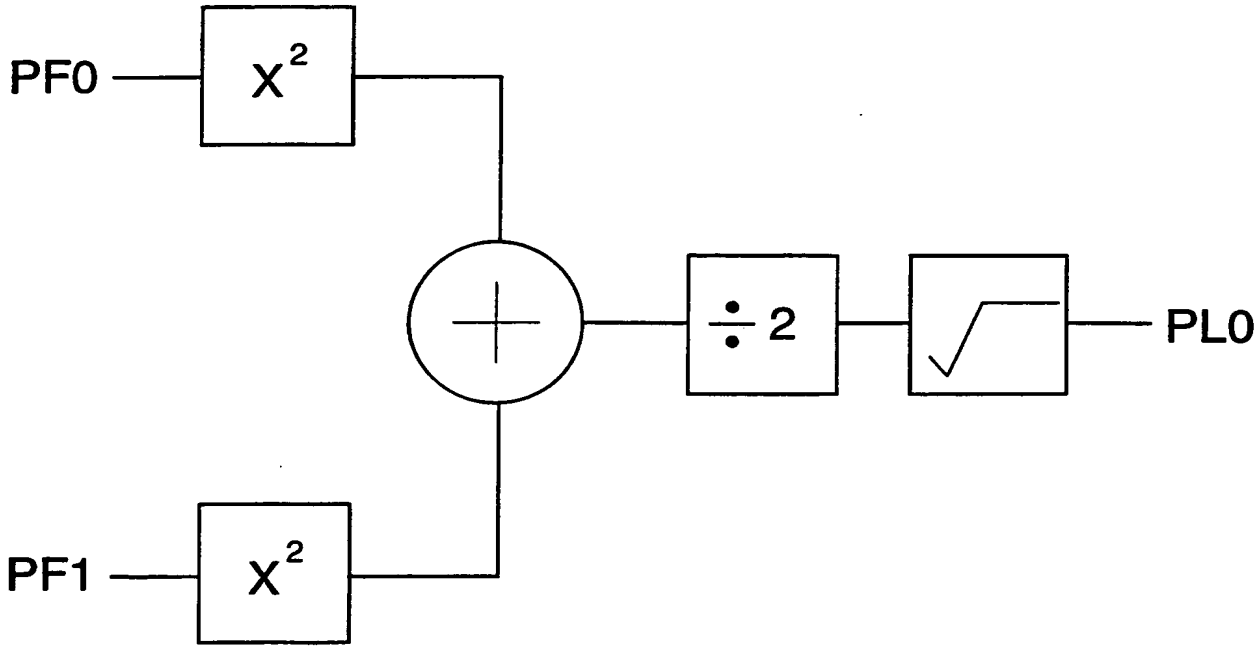


Fig 4.

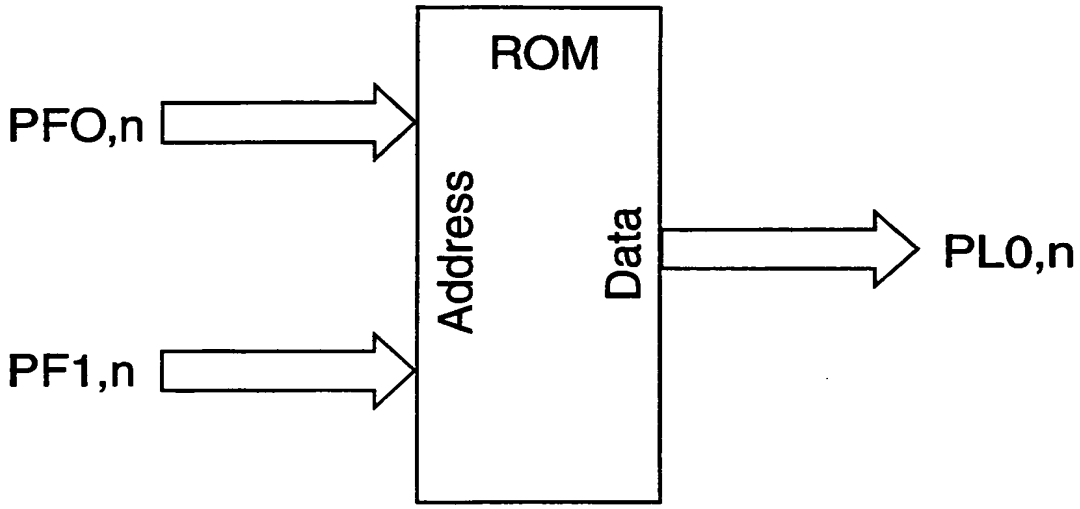


Fig 5.

Difference Between AandB
A>B

	0	10	20	30	40	50	60	70	80	90	100
0	0	10	20	30	40	50	60	70	80	90	100
10		0	10	20	30	40	50	60	70	80	90
20			0	10	20	30	40	50	60	70	80
30				0	10	20	30	40	50	60	70
40					0	10	20	30	40	50	60
50						0	10	20	30	40	50
60							0	10	20	30	40
70								0	10	20	30
80									0	10	20
90										0	10
00											0

Fig 6.

Look Up Table

0	10	20	30	40	50	60	70	80	90
0	5	11	17	23	30	37	45	53	61

Fig 7.

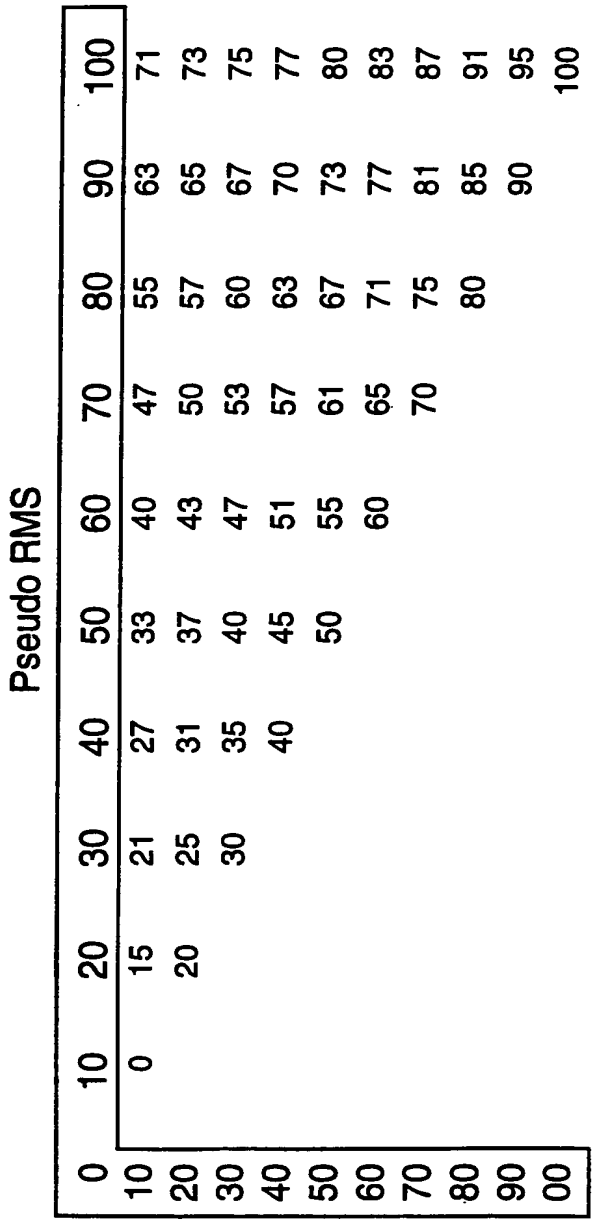
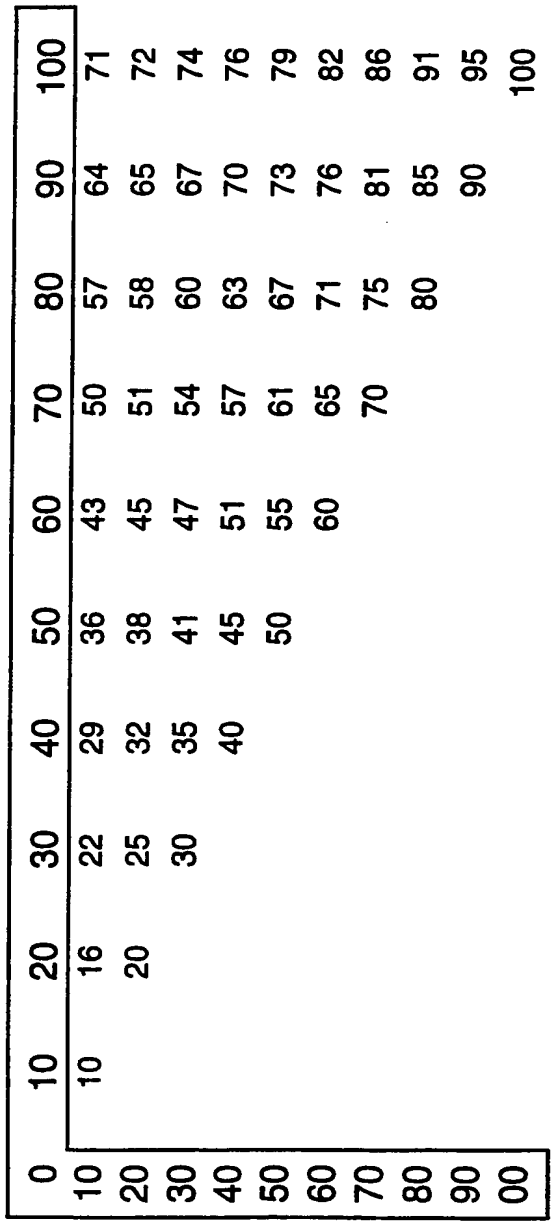

Pseudo RMS
0 10 20 30 40 50 60 70 80 90 100
0 15 21 27 33 40 47 55 63 71
20 25 31 37 43 50 57 65 73
30 35 40 47 53 60 67 75
40 45 51 57 63 70 77
50 55 61 67 73 80
60 65 71 77 83
70 75 81 87
80 85 91
90 95
100

Fig 8.


RMS
0 10 20 30 40 50 60 70 80 90 100
10 16 22 29 36 43 50 57 64 71
20 25 32 38 45 51 58 65 72
30 35 41 47 54 60 67 74
40 45 51 57 63 70 76
50 55 61 67 73 79
60 65 71 76 82
70 75 81 86
80 85 91
90 95
100

INTERNATIONAL SEARCH REPORT

International application No.
PCT/AU00/00673

A. CLASSIFICATION OF SUBJECT MATTER

Int Cl[7]: H04N 3/00, 7/01, 9/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC: H04N, G06T

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
WPAT: value, amplitude, hue, colour, component, pixel, root mean square (rms).

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 596 371 A (PAKHCHYAN et al), 21 January 1997 Whole document	1-30
A	US 5 493 338 A (HONG), 20 February 1996 Whole document	1-30
A	US 5 347 314 A (FAROUDJA et al). 13 September 1994 Whole document	1-30

[X] Further documents are listed in the continuation of Box C

[X] See patent family annex

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"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
31 July 2000	22 AUG 2000
Name and mailing address of the ISA/AU AUSTRALIAN PATENT OFFICE PO BOX 200 WODEN ACT 2606 AUSTRALIA E-mail address: pct@ipaustralia.gov.au Facsimile No.: (02) 6285 3929	Authorized officer **MANISH RAJ** Telephone No.: (02) 6283 2175

INTERNATIONAL SEARCH REPORT

International application No.
PCT/AU00/00673

C (Continuation). **DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 168 358 A (ISHIZU et al), 1 December 1992 Whole document	1-30
A	US 5 159 451 A (FAROUDJA et al), 27 October 1992 Whole document	1-30
A	US 5 001 651 A (REHME et al), 19 March 1991 Whole document	1-30
A	US 4 989 090 (CAMPBELL et al), 29 January 1991 Whole document	1-30
A	US 4 967 271A (CAMPBELL et al), 30 October 1990 Whole document	1-30
A	US 4 876 596A (FAROUDJA), 24 October 1989 Whole document	1-30

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/AU00/00673

This Annex lists the known "A" publication level patent family members relating to the patent documents cited in the above-mentioned international search report. The Australian Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

Patent Document Cited in Search Report		Patent Family Member	
US	5 596 371A	NONE	
US	5 493 338A	NONE	
US	5 347 314A	US	5 159 451
		WO	92/17028
		US	5 488 422
US	5 168 358A	EP	460928
		JP	4040878
		JP	4196788
US	5 159 451A	WO	92/17028
		US	5 347 314
		US	5 488 422
US	5 001 651A	NONE	
US	4 989 090	EP	391094
		JP	2294191
		JP	11346347
		US	4 967 271
US	4 967 271	EP	391094
		JP	2294191
		JP	11346347
		US	4 989 090
US	4 876 596	NONE	

END OF ANNEX